

**WHAT IS CLAIMED IS:**

1           1.       A closed circuit television (CCTV) system comprising:  
2           a number of cameras for generating picture signals;  
3           a multiplexer allotting identification information to each of the picture signals received from  
4           the cameras, said identification information being represented by a predetermined number of bits  
5           so that a number of available identifications is twice or more than the number of the cameras, said  
6           identification information comprising a plurality of proper identification bits and a corresponding  
7           plurality of auxiliary bits, characterized in that the proper identification bits identify which camera  
8           generated a corresponding picture signal; and

9           a picture signal storage medium for storing the picture signals and allotted identification  
10          information output from the multiplexer.

11          2.       The CCTV system as set forth in claim 1, wherein said multiplexer is a parallel to  
12          serial multiplexer.

13          3.       The CCTV system as set forth in claim 1, wherein the picture signal storage medium  
14          comprises a single video tape in a single video tape recorder.

15          4.       The CCTV system as set forth in claim 1, wherein the picture signal storage medium  
16          comprises a single digital storage medium.

17          5.       The CCTV system as set forth in claim 1, further comprising:

2 a monitor for displaying picture signals reproduced by said picture signal storage medium;  
3 and  
4 a selection unit for enabling a user to select picture signals corresponding to a particular one  
5 of said cameras for display on said monitor by inputting the identification information corresponding  
6 to said particular one of said cameras.

1 6. The CCTV system as set forth in claim 5, further comprising:  
2 a controller for storing said picture signals and said identification information in said picture  
3 signal storage medium, said controller being responsive to a selection signal generated by said  
4 selection unit for selecting the picture signals corresponding to said particular one of said cameras  
5 and stored in said picture signal storage medium and outputting the selected picture signals for  
6 display on said monitor.

1 7. The CCTV system as set forth in claim 1, wherein the logical values of said auxiliary  
2 bits are opposite to the logical values of said proper identification bits.

1 8. The CCTV system as set forth in claim 7, wherein the number of cameras is four and  
2 the identification information comprises two said proper identification bits and two said auxiliary  
3 bits.

1 9. The CCTV system as set forth in claim 8, wherein said proper identification bits and  
2 said auxiliary bits are disposed in an alternating arrangement such that said auxiliary bits are the least  
3 significant and second most significant bits in said arrangement and said proper identification bits

are the second least significant bits and the most significant bit in said arrangement.

10. The CCTV system as set forth in claim 7, wherein the number of cameras is eight and the identification information comprises three said proper identification bits and three said auxiliary bits.

11. The CCTV system as set forth in claim 10, wherein said proper identification bits and said auxiliary bits are disposed in an predetermined arrangement such that said auxiliary bits are the least significant bits in said arrangement and said proper identification bits are the most significant bit in said arrangement.

12. The CCTV system as set forth in claim 1, wherein the logical values of said auxiliary bits are identical to the logical values of said proper identification bits.

13. The CCTV system as set forth in claim 12, wherein the number of cameras is four and the identification information comprises two said proper identification bits and two said auxiliary bits.

14. The CCTV system as set forth in claim 13, wherein said proper identification bits and said auxiliary bits are disposed in an alternating arrangement such that said auxiliary bits are the least significant and second most significant bits in said arrangement and said proper identification bits are the second least significant bits and the most significant bit in said arrangement.

1           15.    The CCTV system as set forth in claim 12, wherein the number of cameras is eight  
2    and the identification information comprises three said proper identification bits and three said  
3    auxiliary bits.

1           16.    The CCTV system as set forth in claim 15, wherein said proper identification bits and  
2    said auxiliary bits are disposed in an predetermined arrangement such that said auxiliary bits are the  
3    least significant bits in said arrangement and said proper identification bits are the most significant  
4    bit in said arrangement.

1           17.    A closed circuit television (CCTV) system comprising:  
2    a number of cameras for generating picture signals;  
3    a multiplexer allotting identification information to each of the picture signals received from  
4    the cameras, said identification information being represented by a predetermined number of bits  
5    so that a number of available identifications is twice or more than the number of the cameras; and  
6    a picture signal storage medium for storing the picture signals and allotted identification  
7    information output from the multiplexer.

1           18.    The CCTV system as set forth in claim 15, wherein said identification information  
2    comprises a plurality of proper identification bits specific to each of the cameras and a corresponding  
3    plurality of auxiliary bits.

1           19.    The CCTV system according to claim 18, wherein the logical values of the auxiliary  
2    bits are reversed from the logical values of the proper identification bits.

1           20.     The CCTV system according to claim 18, wherein the logical values of the auxiliary  
2 bits are identical to the logical values of the proper identification bits.

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